

Date: March 19, 2004
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<p>U.S. DEPARTMENT OF COMMERCE (REV. 05/03) PATENT AND TRADEMARK OFFICE</p> <p>INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i></p> <p><i>RECEIVED U.S. PATENT AND TRADEMARK OFFICE MAR 22 2004 EX-106</i></p>				ATTY. DOCKET NO. TI-37372 (UNITI-181XX)	APPLICATION NO. 10/724,296		
				APPLICANT: Nitin Agarwal et al			
				FILING DATE November 28, 2003	TC ART UNIT UnAssigned		
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION/ ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE
		US					
		US					
		US					
		US					
		US					
		US					
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)							
<i>mr</i>		Rabaey, J. M., "Digital Integrated Circuits: A Design Perspective" Published by Prentice-Hall, Inc., Second Edition, September 2000.					
EXAMINER	<i>B. K. K. J.</i>			DATE CONSIDERED <i>8/16/04</i>			
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							